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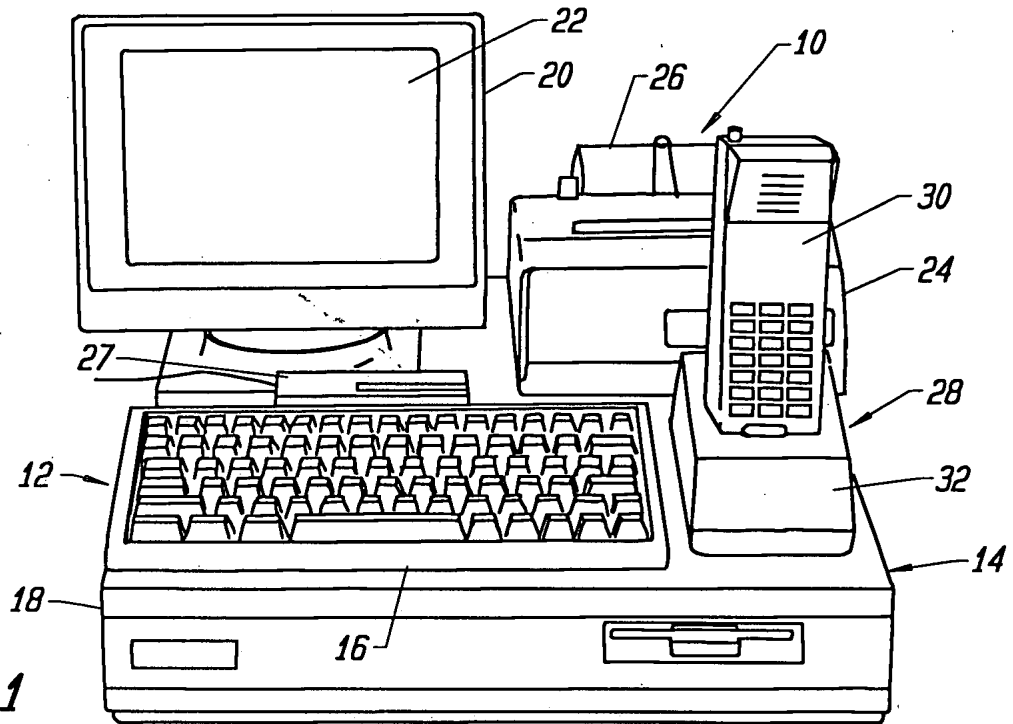


FIG. 1

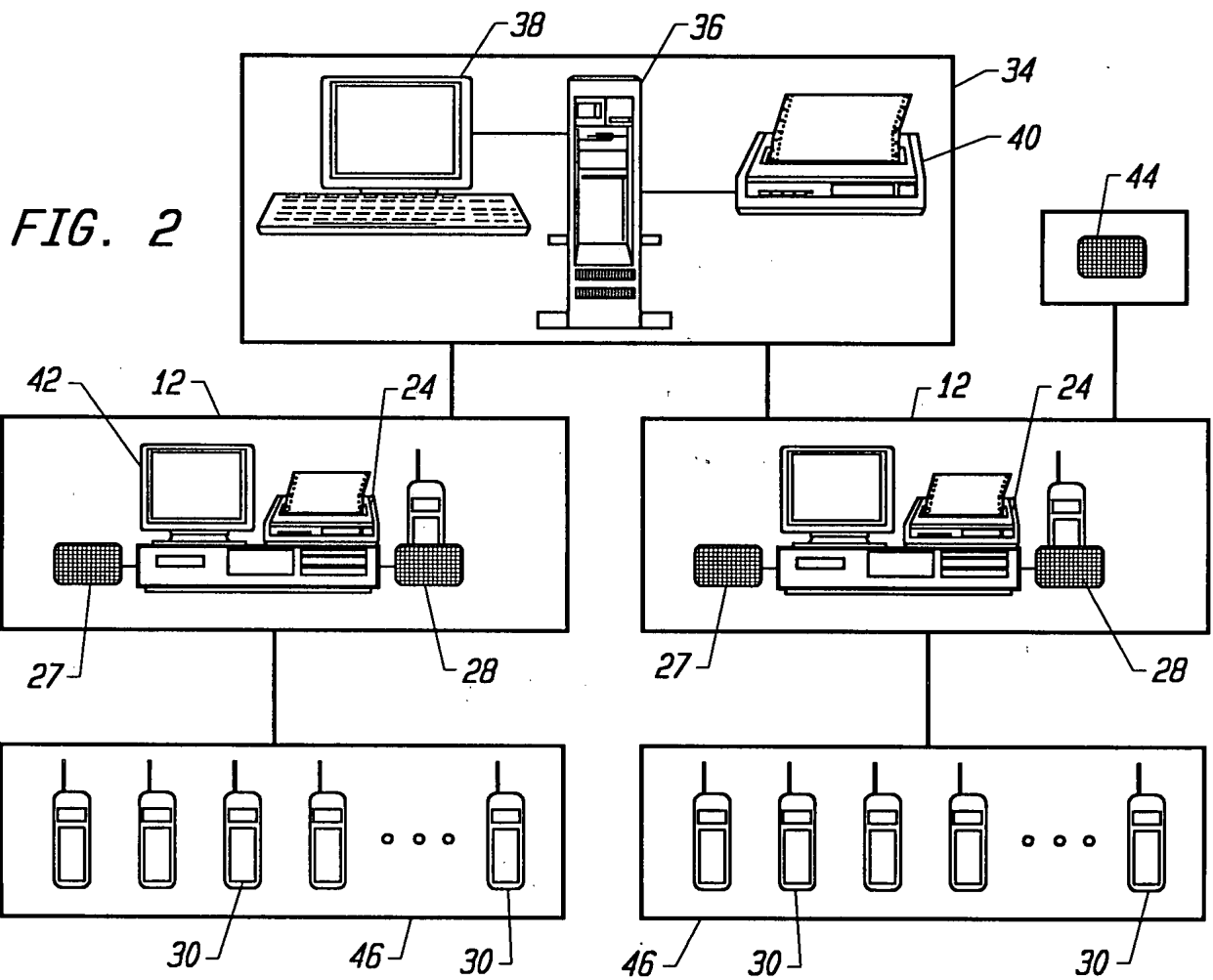


FIG. 2

FIG. 2
CLASS SUBCLASS
379 59
CRAFTSMAN

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BY			
DRAFTSMAN			

FIG. 3

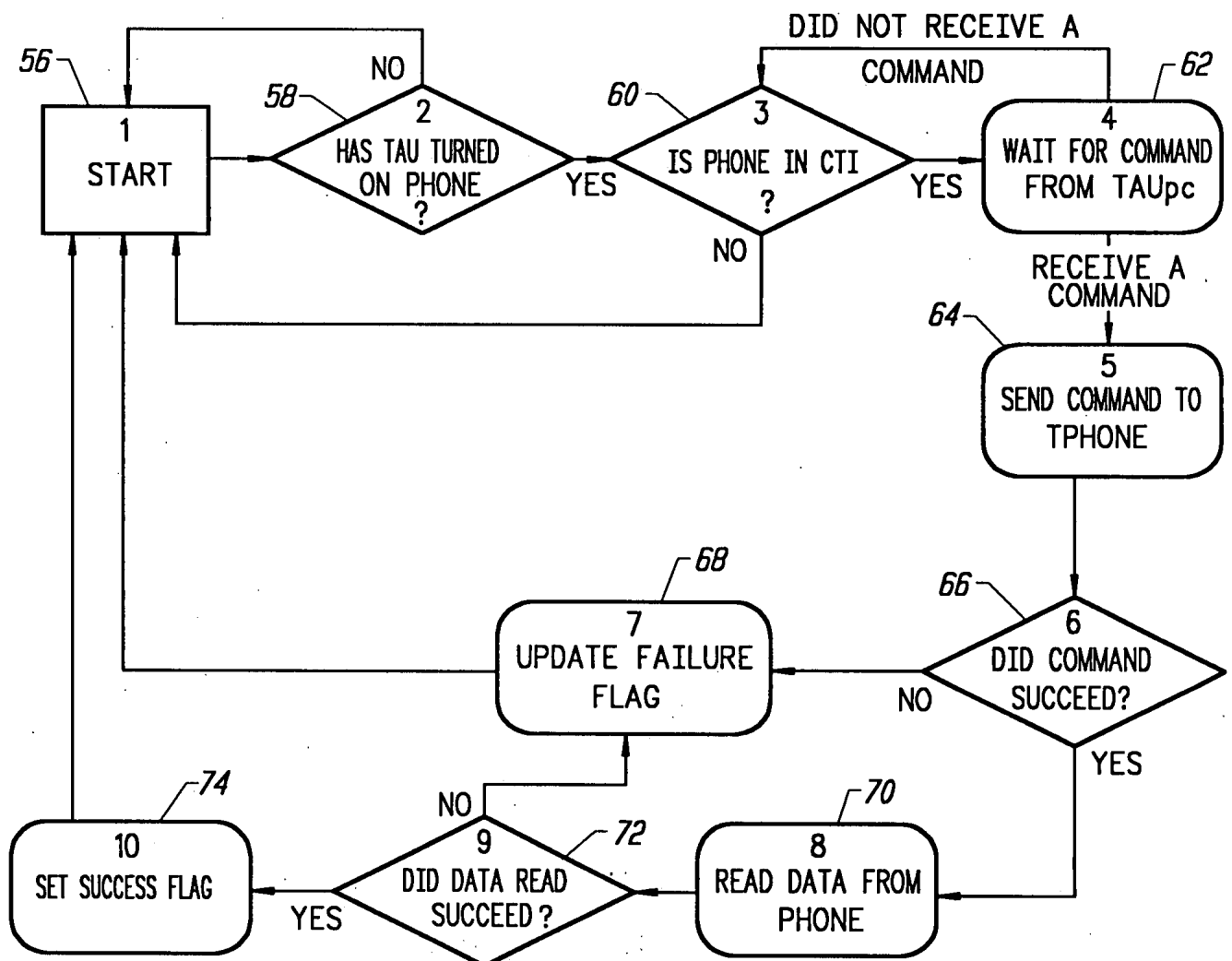
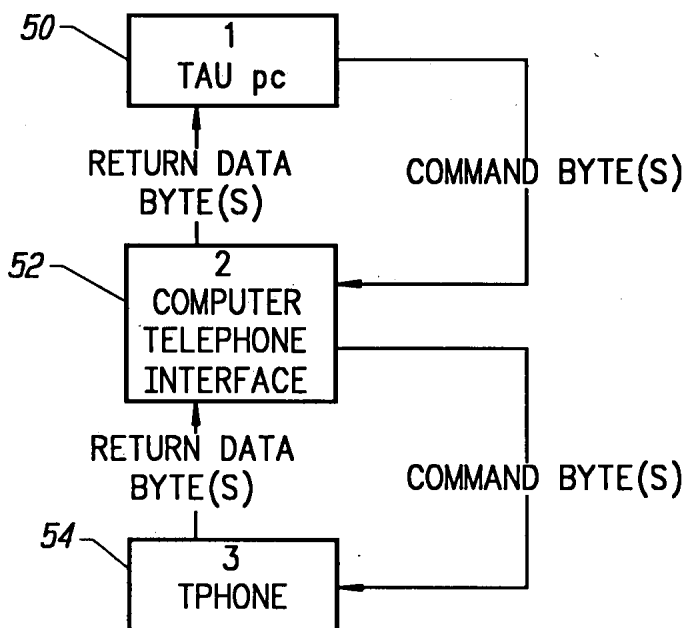


FIG. 4

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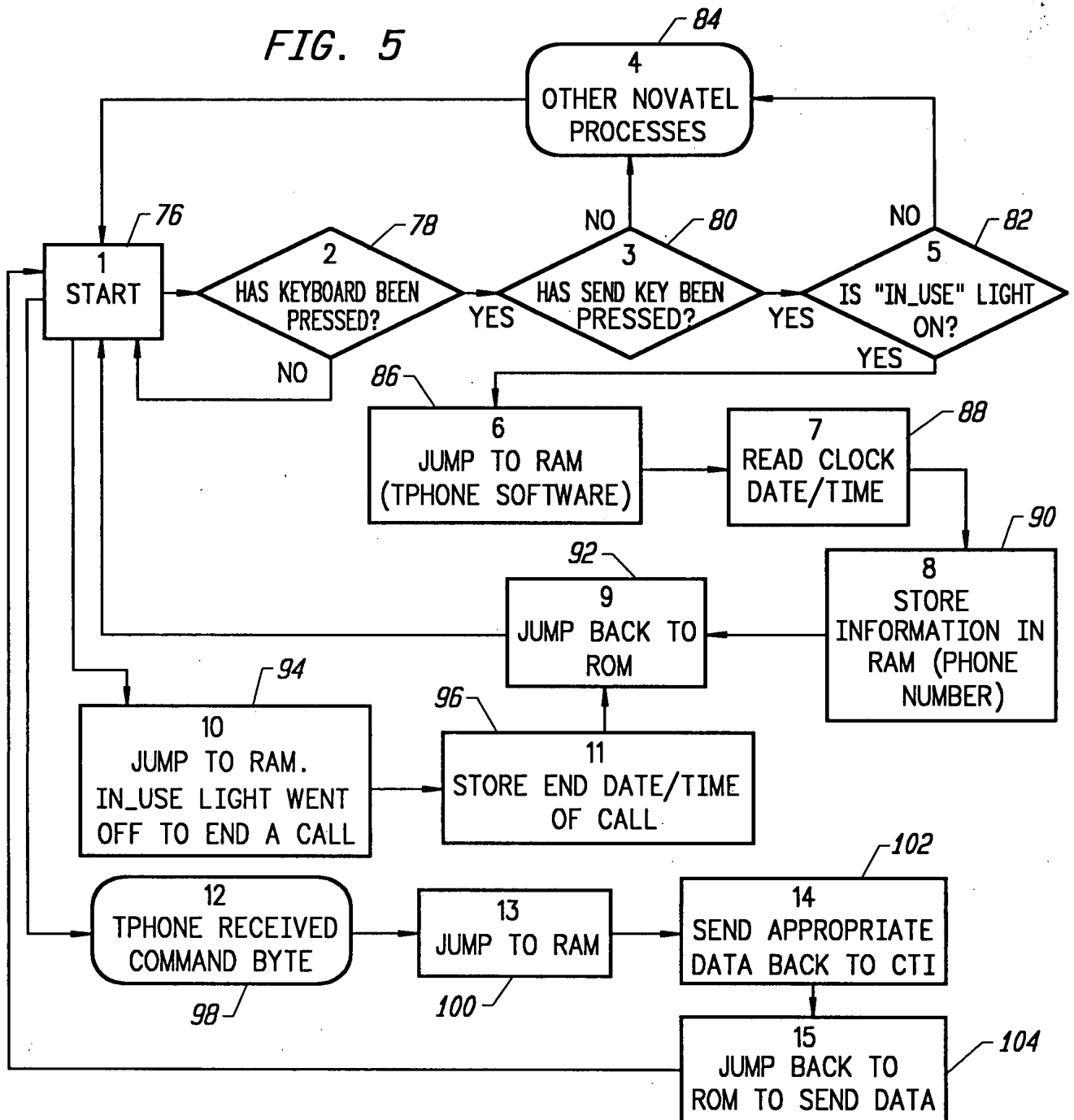


FIG. 6

The diagram illustrates a system architecture with the following components and connections:

- CPU (14)**: A central processing unit connected to the Micro-processor via a bidirectional arrow.
- MICRO-PROCESSOR (202)**: A component that interfaces with the CPU and the Translation means via a bidirectional arrow.
- TRANSLATION MEANS (210)**: A component that receives input from the Micro-processor and the Telephone unit, and outputs to the Program and the Telephone unit.
- PROGRAM (212)**: A component that receives input from the Translation means.
- TELEPHONE UNIT (208)**: A component that interfaces with the Translation means via a bidirectional arrow.

The components are interconnected as follows:

- The CPU (14) is connected to the MICRO-PROCESSOR (202) via a bidirectional arrow.
- The MICRO-PROCESSOR (202) is connected to the TRANSLATION MEANS (210) via a bidirectional arrow.
- The TRANSLATION MEANS (210) is connected to the TELEPHONE UNIT (208) via a bidirectional arrow.
- The TRANSLATION MEANS (210) is connected to the PROGRAM (212) via a downward arrow.

The components are labeled with reference numerals: CPU (14), MICRO-PROCESSOR (202), TRANSLATION MEANS (210), PROGRAM (212), and TELEPHONE UNIT (208). The entire system is enclosed in a dashed box labeled 204.

ATTENTION: THIS FIG. 7A
 IS A SUBCLASS
 OF FIG. 7A

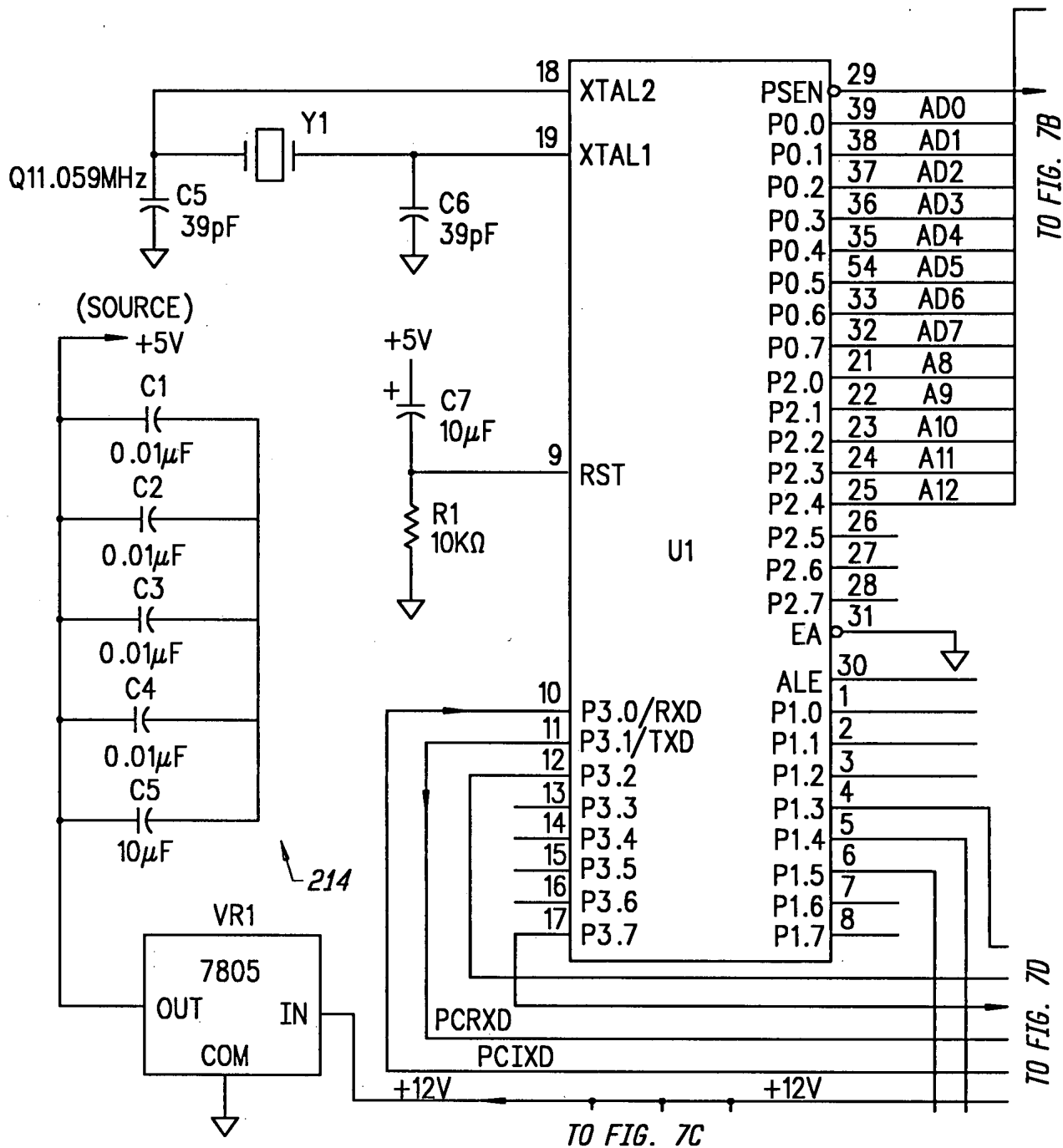
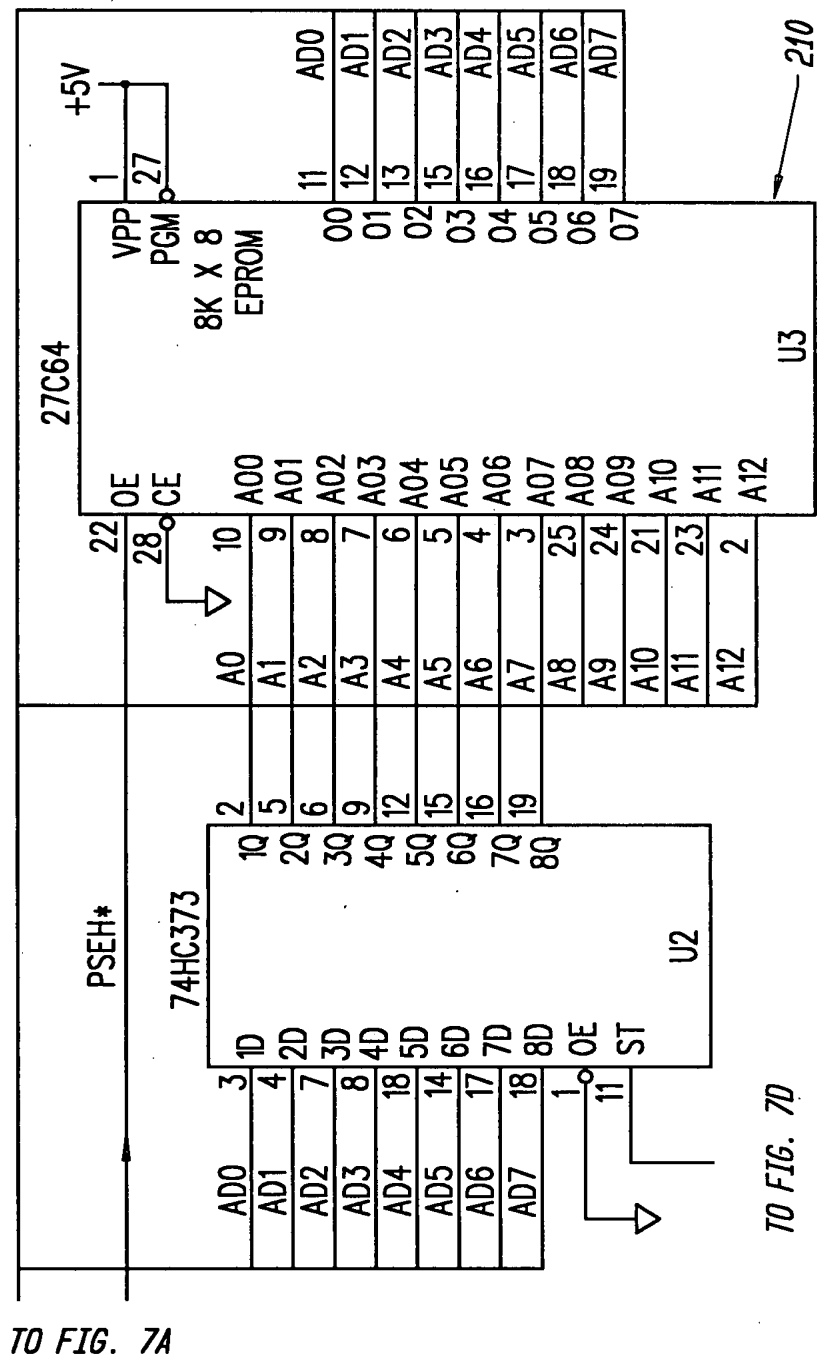


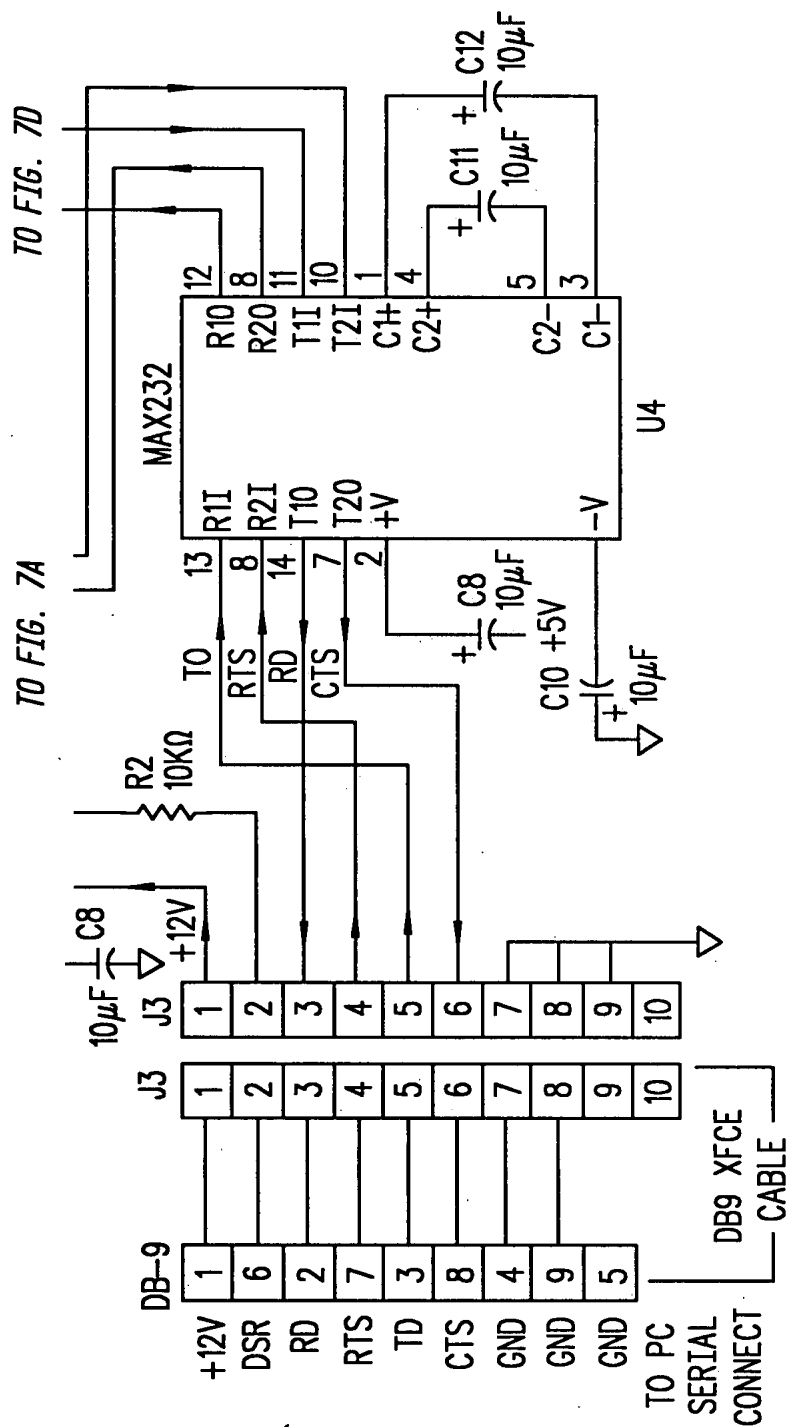
FIG. 7A



TO FIG. 7A

TO FIG. 7D

FIG. 7B



APPROVED BY DRAFTSMAN
 J.C. FIG. 7D
 CLASS SUBCLASS

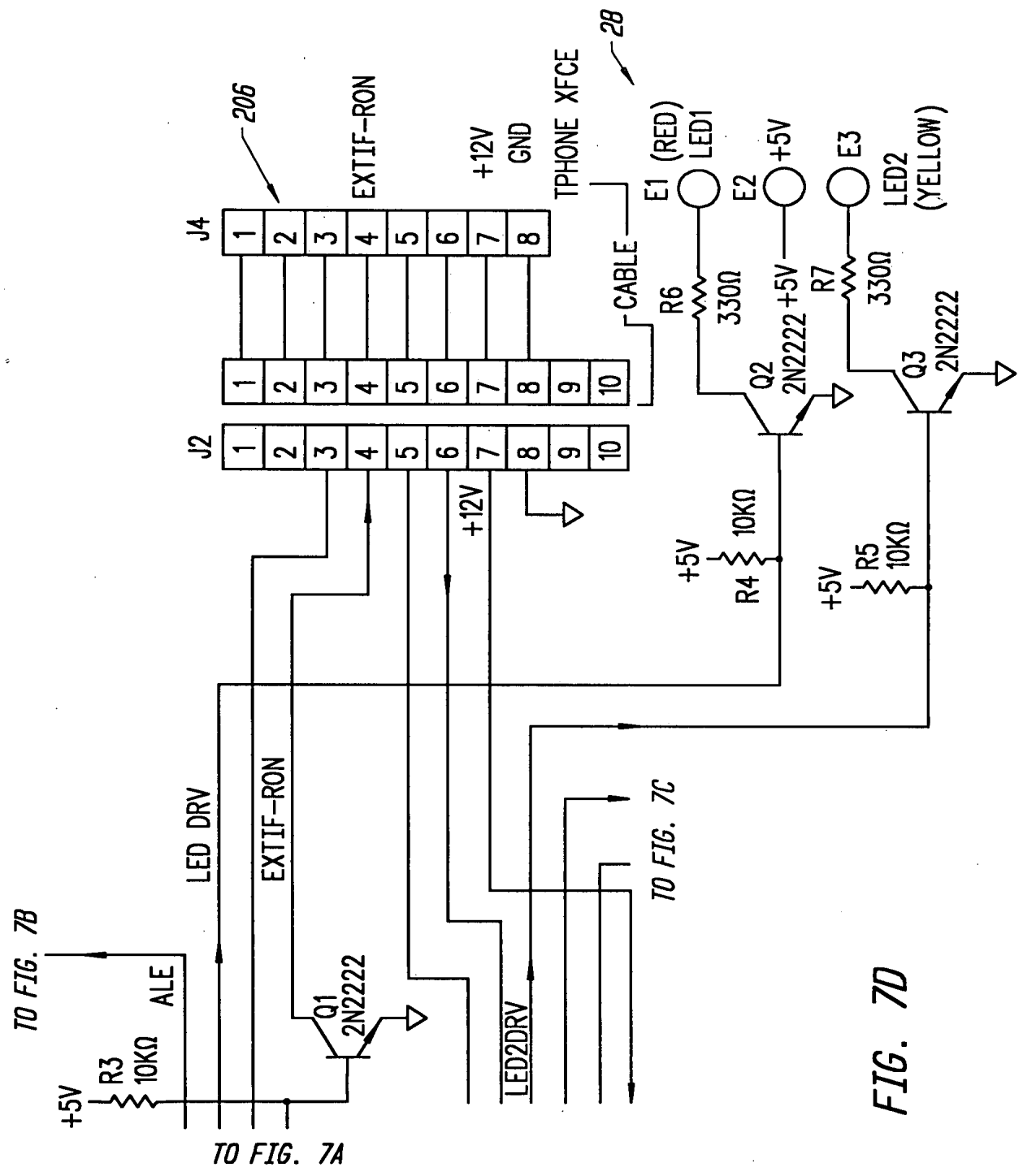


FIG. 7D

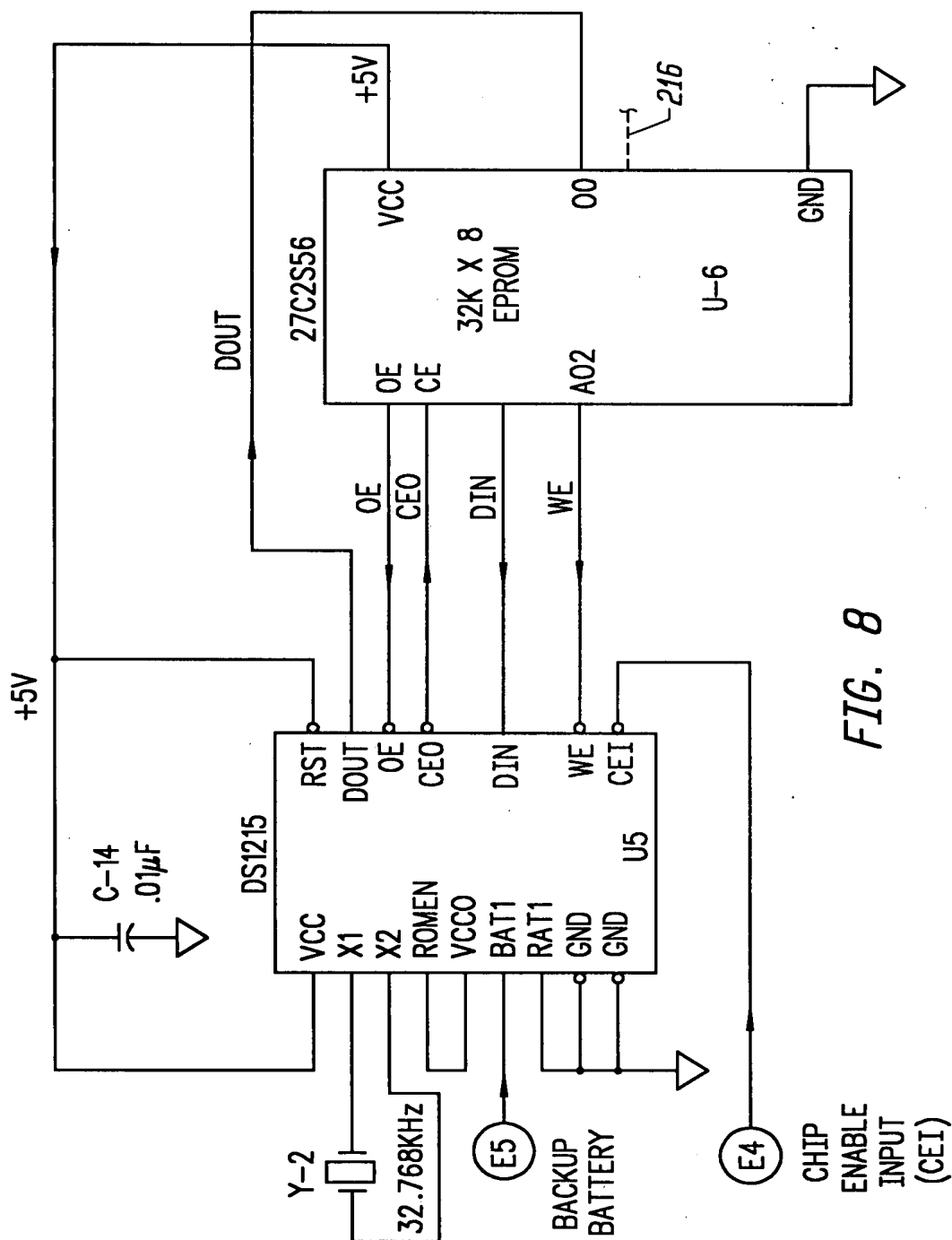


FIG. 8